



COURSE DESCRIPTION CARD - SYLLABUS

Course name

Workshop [S2EiT1-SKiTI>PRAC]

Course

Field of study

Electronics and Telecommunications

Year/Semester

1/2

Area of study (specialization)

Computer Networks and Internet Technologies

Profile of study

general academic

Level of study

second-cycle

Course offered in

polish

Form of study

full-time

Requirements

compulsory

Number of hours

Lecture

0

Laboratory classes

0

Other (e.g. online)

0

Tutorials

0

Projects/seminars

30

Number of credit points

2,00

Coordinators

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Lecturers

prof. dr hab. inż. Wojciech Kabaciński
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Prerequisites

Basic and in-depth knowledge in the field of electronics and telecommunications, knowledge and skills in computer programming, the ability to critically view sources, awareness of the need for scientific work.

Course objective

The workshop is a preparatory stage for the implementation of the master's thesis. During the course the scope of the diploma thesis is precisely defined, a critical review of the literature and/or existing solutions is carried out. The goal is to develop practical problem-solving skills in the field of knowledge acquired during studies.

Course-related learning outcomes

Knowledge:

1. Student knows the basics of scientific work
2. Student knows the methods and tools for solving the complex problems in engineering or research tasks in selected areas of electronics and telecommunications
3. Student is aware of the source citations and the need for independent work

Skills:

1. Student is able to use various sources of information, can make a critical analysis of information in the field of selected areas of electronics and telecommunications,
2. Student is able to draw conclusions, and formulate and justify opinions
3. Student is able to prepare and present a presentation of his task and to initiate, lead and summarize the discussion about it

Social competences:

1. Student can initiate and control discussion on selected technical topics, is able to formulate and defend judgments
2. Student is able to plan activities related to the implementation of work on a complex task in the field of electronics and telecommunications

Methods for verifying learning outcomes and assessment criteria

Learning outcomes presented above are verified as follows:

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A control of the progress of the work on creating a diploma thesis by:

1. Preparation of at least two presentations indicating the progress of the work in the subsequent stages of its creation
2. Speech based on the presentation of the work and participation in the discussion about it

The following components are subject to evaluation:

1. Class attendance
2. Activity during classes, involvement in discussions, ability to defend one's position
3. Quality of presentations
4. Ability to deliver the speech
5. Timeliness of task implementation

The final grade is the result of component grades, with each component grade being positive. The rating scale from 2 (insufficient – negative) to 5 (very good) is used for component grades and for the final grade.

Programme content

Conducting an analysis of the problem that is the subject of the master's thesis, including a critical review of the literature and existing solutions.

Teaching methods

Case-study, presentation, oral presentation of a work, participation in a discussion, discussion control, seminar lecture using a whiteboard and/or projector.

Bibliography

Basic

1. Dudziak A., Żejmo A.: Redagowanie prac dyplomowych – wskazówki metodyczne dla studentów. Difin, Warszawa 2008 (in Polish)

1. Zenderowski R.: Praca magisterska-Licencjat. Krótki przewodnik po metodologii pisania i obrony pracy dyplomowej, CeDeWu Sp. z o.o., 2015 (in Polish)

Additional

Breakdown of average student's workload

	Hours	ECTS
Total workload	60	2,00
Classes requiring direct contact with the teacher	40	1,00
Student's own work (literature studies, preparation for laboratory classes/ tutorials, preparation for tests/exam, project preparation)	20	1,00